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RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP ART UNIT 2817

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No: 09/596,863
Filed: June 19, 2000
Inventor(s):
Novak, Istvan

Examiner: Lee, B.
Group/Art Unit: 2817
Atty. Dkt. No: 5181-62800

Title: BYPASS CAPACITOR
METHODS FOR
ACHIEVING A DESIRED
VALUE OF ELECTRICAL
IMPEDANCE BETWEEN
PARALLEL PLANAR
CONDUCTORS OF AN
ELECTRICAL POWER
DISTRIBUTION
STRUCTURE AND
ASSOCIATED
ELECTRICAL POWER
DISTRIBUTION
STRUCTURES

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, DC 20231, on the date indicated below.

Erik A. Heter
Printed Name
Signature Date 8/4/03

Approved for Entry
PE

AMENDMENT; RESPONSE TO OFFICE ACTION OF May 7, 2003

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

This paper is submitted in response to the Office Action of May 7, 2003, to further highlight why the application is in condition for allowance.